

PTO/SB/08A (08-03)

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Substitute for form 1-27

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/712,229
Filing Date	Nov. 12, 2003
First Named Inventor	Van Brocklin et al.
Art Unit	2871 2629
Examiner Name	Not assigned P. DHARIA
Attorney Docket Number	200309795-1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 5,644,327	07-01-1997	Onyskevych et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ³
		Country Code ⁴ Number ⁴ Kind Code ⁵ (if known)				
		WO02/47310	06-13-2002	Sarnoff Corp		<input checked="" type="checkbox"/>
		DE19950839	05-23-2001	Fraunhofer		<input type="checkbox"/>
		WO03/091798	11-06-2003	Sipix Imaging		<input checked="" type="checkbox"/>
						<input type="checkbox"/>
						<input type="checkbox"/>
						<input type="checkbox"/>

Examiner
Signature

P. DHARIA

Date
Considered

06-21-06

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO

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Complete if Known

Application Number	10/712.229
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Filing Date	Nov. 12, 2003
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First Named Inventor	Van Brocklin et al.
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Art Unit

Examiner Name

Attorney Docket Number	200309795-1
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Sheet

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**Examiner
Signature**

Date
Considered

06-21-06

*EXAMINER: Initial references considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). **2** Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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PATENT APPLICATION

Sheet 1 of 4

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

200309795-1

APPLICATION NO.

07/2229

CONFIRMATION NO.

APPLICANT

Van Brocklin, et al.

FILING DATE

11-12-2003

GROUP

2629

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	5,235,451	08/10/93	Bryan	
	1B	5,514,618	05/07/96	Hunter, Jr., et al.	
	1C	5,585,695	12/17/96	Kitai	
	1D	5,952,948	09/14/99	Proebsting	
	1E	5,986,737	11/16/99	Evanicky, et al.	
	1F	6,194,837	02/27/01	Ozawa	
	1G	6,232,937	05/15/01	Jacobsen, et al.	
	1H	6,268,842	07/31/01	Yamazaki, et al.	
	1I	6,300,988	10/09/01	Ishihara, et al.	
	1J	6,359,606	03/19/02	Yudasaka	
	1K	6,364,450	04/02/02	Yamaguchi, et al.	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L				
	1M				
	1N				
	1O				
	1P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	Breakthrough Proprietary Semiconductor Technologies; www.iowathinfilm.com/technology/index.html ; taken from referenced cite 10/06/03
	1R	Philips Demonstrates Video-speed Electronic-paper Technology Based on Electrowetting; www.sciencedaily.com/releases/2003/09/030926070504.htm ; taken from referenced cite 10/06/03
	1S	Sharp 7" LCD Glass integrates Drivers, Controller; http://neasia.nikkeibp.com/nea/200103/peri_124539.html ; takend from referenced cite 10/22/03

EXAMINER

P. DHARIA

DATE CONSIDERED

06-21-06

PATENT APPLICATION

Sheet 2 of 4

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
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ATTY. DOCKET NO.

200309795-1

APPLICATION NO.

10712 229

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Van Brocklin, et al.

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REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	2A	6,476,988	11/05/02	Yudasaka	
	2B	6,525,467	02/25/03	Eida, et al.	
	2C	6,525,788	02/25/03	Nakagawa, et al.	
	2D	6,538,390	03/25/03	Fujita, et al.	
	2E	6,552,704	04/22/03	Zavracky, et al.	
	2F				
	2G				
	2H				
	2I				
	2J				
	2K				

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	2L					
	2M					
	2N					
	2O					
	2P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	2Q	Sharp, SEL integrate processor into system-on-glass device; www.eetimes.com/at/news/OEG20021023S0048; taken from referenced cite 10/22/03
	2R	Low Temperature Polysilicon; www.electroline.com/au/elc/feature_article/item_052002b.asp; taken from referenced cite 10/22/03
	2S	What is Electronic Ink? www.eink.com; taken from referenced cite 10/06/03

EXAMINER

P. DHARIA

DATE CONSIDERED

06-25-06

PATENT APPLICATION

Sheet 3 of 4

FORM PTO-1449

**LIST OF PATENTS AND PUBLICATIONS FOR
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ATTY. DOCKET NO.

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APPLICATION NO.

10712229

CONFIRMATION NO.

APPLICANT

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	3B				
	3C				
	3D				
	3E				
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	3L					
	3M					
	3N					
	3O					
	3P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	3Q	Hug, et al; An Overview of LVDS Technology; National Semiconductor Application Note 971; July 1998
	3R	R.C. Liang; Microcup Electrophoretic and Liquid Crystal Displays; SiPix Imaging, Inc.
	3S	Greenfield, et al. A Lamination Study of a Composite LCD Flat Panel Display; International Society Conference on Thermal Phenomena; 2002

EXAMINER

P. DHARIA

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PATENT APPLICATION

Sheet 4 of 4

FORM PTO-1449

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ATTY. DOCKET NO.

200309795-1

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1071229

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	4L				
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	4N				
	4O				
	4P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

4Q	Neuburger, et al.; A New Interlayer Interconnect Technology for Multilayer Fabrication; International Conference of Multichip Modules and High Density Packaging; 1998
4R	Jung, et al.; Chip-in-Polymer: Volumetric Packaging Solution using PCB Technology; SEMI Technology Symposium: International Electronics Manufacturing Technology (IEMT) Symposium; 2002
4S	

EXAMINER

J. DHARIA

DATE CONSIDERED

06/21/06